

AMENDMENTS TO THE CLAIMS

1. (Currently amended): A reconfigurable digital processor for film conversion, the reconfigurable digital processor comprising: a plurality of reprogrammable circuit elements in a circuit which is laid out in three or more groups of reprogrammable elements where the groups are configured to receive digital data corresponding to film image pixels and perform ~~one~~two or more film conversion processing steps on the film image pixels using digital technology and where the number of elements in each group can be increased to improve the performance or can be reduced to decrease the cost of the system; a supervisor control circuit configured to communicate with and instruct the plurality of reprogrammable circuit elements on sequencing of the film conversion processing steps; and a formatter configured to receive one or more outputs from the plurality of reprogrammable circuit elements and produce one or more digital motion picture files.

2. (Original): The reconfigurable digital processor of claim 1, wherein software codes are used to reconfigure one or more of the reprogrammable circuit elements to support a different film conversion processing step.

3. (Original): The reconfigurable digital processor of claim 1, wherein the reprogrammable circuit elements are field programmable gate arrays.

4. (Original): The reconfigurable digital processor of claim 1, wherein the reprogrammable circuit elements are digital signal processors.

5. (Original): The reconfigurable digital processor of claim 1, wherein the number of

reprogrammable circuit elements is chosen to trade-off throughput and cost.

6. (Original): The reconfigurable digital processor of claim 1, wherein at least one of the digital motion picture files is processed by a digital-to-analog converter for display on a video monitor.

7. (Original): The reconfigurable digital processor of claim 1, wherein at least one of the digital motion picture files is provided to a digital monitor for viewing.

8. (Original): The reconfigurable digital processor of claim 2, wherein reconfiguration occurs during a film conversion session.

9. (Original): The reconfigurable digital processor of claim 2, wherein reconfiguration is used to trade-off complexity of film conversion processing steps and throughput rates of the one or more digital motion picture files.

10. (Currently amended): A method of processing film images using digital technology, the method comprising: using mechanical and optical devices to provide images from a sequence of film frames to two or more photo-sensor arrays; controlling the operations of the photo-sensor arrays to be synchronous with movement of the film frames; digitizing analog signals produced by the two or more photo-sensor arrays; providing the digitized analog signals to a digital processing core comprising a plurality of reprogrammable circuit elements in a circuit which is laid out in three or more groups of reprogrammable elements and where the number of elements in each group can be increased to improve the performance or can be reduced to decrease the cost of the system for one or more film processing functions; and manipulating one or more outputs from the digital-processing core to produce a digital motion picture file conforming to a standard

format.

11. (Original): The method of claim 10, wherein the digital processing core is a combination of field programmable gate arrays and digital signal processors.

12. (Original): The method of claim 10, wherein the digital processing core is reconfigurable with software codes to perform different film processing functions.

13. (Original): The method of claim 12, wherein the digital processing core is reconfigured to adapt to a change in the standard format of the digital motion picture file.

14. (Currently amended): A method of building a cost-efficient film conversion device, the method comprising: using photosensitive detectors to produce electrical signals corresponding to film pixels; coupling analog-to-digital converters to the photosensitive detectors to digitize the electrical signals; using a circuit layout for the processing elements having three or more groups of reprogrammable circuit elements where the groups are arranged to perform sequential processing functions; and scaling the number of programmable electronic circuit elements in each of said groups of the film conversion device based on cost and performance constraints; and customizing the programmable electronic circuits using software codes to perform one or more film conversion functions on the digitized electrical signals.

15. (Original): The method of claim 14, wherein one or more programmable electronic circuit elements are reconfigurable using software codes during a film conversion session to support different film conversion functions.

16. (Currently amended): A device for generating electrical signals representing images recorded on film to an electronic format, said device comprising: a platform supporting film reels; a scanning module separate from said platform comprising an illuminating subassembly, a film guide subassembly, and an image subassembly; said illumination subassembly including a lamp; said film guide subassembly including a guide having a slit over which said film passes, said slit being illuminated by said illumination subassembly; said imaging subassembly including an array of photosensitive detectors that receive light that passes through said slit and said film and outputs electrical analog signals corresponding to respective pixels in said film image; an analog-to-digital converter configured to produce digital values to represent said analog signals; a plurality of reprogrammable circuit elements in a circuit which is laid out in three or more groups of reprogrammable elements where the groups are configured to receive digital data corresponding to film image pixels and perform ~~one~~two or more film conversion processing steps on the film image pixels using digital technology and where the number of elements in each group can be increased to improve the performance or can be reduced to decrease the cost of the system; a supervisor control circuit configured to communicate with and instruct the plurality of reprogrammable circuit elements on sequencing of the film conversion processing steps; and a formatter configured to receive one or more outputs from the plurality of reprogrammable circuit elements and produce one or more digital motion picture files.